RF IC Design Engineers

Job Responsibilities:
- Design of circuit building blocks for RFIC solutions.
- High performance circuit development for RF transceiver, frequency synthesizer and VCO.

Requirements:
- PhD/Masters/Bachelor in Science with major in EE or Telecommunication Engineering, particularly, in the related fields of Analog or RFIC designs.
- Minimum 3 years’ of experience in designing a wide range of circuit functions for RFIC, such as RF circuits – LNA, Mixer, Modulator, RF PGA, Power Amplifier, etc.
- Frequency synthesizer – Integer-N or Fractional-N PLL, PFD/CP, VCO, Crystal Oscillator, frequency/phase calibration algorithm, etc.
- Analog circuits – VGA, Filter techniques, ADC, DAC, etc.
- Strong analytical skills in optimizing a particular Analog/RFIC circuit topology to meet its block level specifications.
- Good understanding of CMOS and Bi-CMOS process technology.
- Solid foundation in RF theory and circuit analysis.

Analog/ Mixed-Signal IC Design Engineer

Job Responsibilities:
- Define IP blocks specifications based on system architecture and product application requirements.
- Design analog circuit functions for wireless communication and consumer products, such as ADC/DAC/PGA/PLL/Filter, using standard CMOS process.
- Perform full cycle of IP development responsibilities, from circuit simulation, chip verification to prototype evaluation.
- Ensure adequate communication of activities to all relevant parties.
- Maintain updated IP specification and design documentations for effective multi-party communication.

Requirements:
- PhD/Masters/Bachelor in Science with major in EE or Telecommunication Engineering, particularly, in the related fields of Analog designs.
- Minimum 3 years’ of experience in mixed-signal IP design and IC mass production.
- Experienced in designing circuit functions for mixed-signal circuit, such as ADC/DAC/PGA/PLL/Filter, etc.
- Strong analytical skills in optimizing a particular analog circuit topology to meet its block level specifications; solid foundation in circuit analysis.
- Good understanding in deep sub-micron CMOS process technology.
- Well versed in Cadence design flow, such composer, spectre, spectre-RF, System simulation tools, such as Matlab.

Power Management IC Design Engineer

Job Responsibilities:
- Define IP blocks specifications based on system architecture and product application requirements.
- In charge of power management IC product design and training of new engineers.
- Design circuit functions for power management in wireless communication products, such as low quiescent current and high PSRR LDO/DC-DC step-up/down converter. The related circuit functions are designed with standard CMOS process.
- Perform full cycle of IP development responsibilities, from circuit simulation, chip verification to prototype evaluation.
- Ensure adequate communication of activities to all relevant parties.
- Maintain updated IP specification and design documentations for effective multi-party communication.
- Have good communication, organization, problem solving skills, and able to share design knowledge with team members.

Requirements:
- PhD/Masters/Bachelor in Science with major in EE or Telecommunication Engineering, particularly, in the related fields of Analog designs.
- Minimum 3 years’ of experience in power management IC design and IC mass production.
- Familiar with high-voltage device and high-voltage pad with ESD protection.