Hi Prof Xu, this is Liew, Asst HR Mgr of ST Electronics (Satcom & Sensor Systems) Pte Ltd, a subsidiary of Spore Technologies Group. We hv following 2 job vacancies which may interest your students. Hope you can kindly share with your students:

Career Opportunities
(1) Senior RF Engineer
Job description:
Develop RF/Microwave circuits and modules for ultra-wide bandwidth software defined radio according to the project requirement. Design, analyze, integrate and test RF hardware and its supply circuitry.

Job requirement:
· Strong experience in RF/Microwave MIC circuits & modules design using ADS software included circuit simulator as well as EM simulator. Design, analyze and integration of microwave circuits.
· Hand-on experience in the measurement of RF/Microwave circuits and modules using VNA, Spectrum analyzer, noise analyzer etc.
· Strong R&D background in RF passive designs, such as RF Filters, coupler, is more preferred.
· MMIC design experiences, like ultra-wide bandwidth LNA, PA, are preferred.
· Responsibilities include board-level RF/analog design, analysis, and test.
Design RF and analog PWB/PCBs using Pads from Mentorgraphics.
· Good team worker and self-motivated
· Outstanding communications skills, oral and written

Education Requirements: BSEE above with 3~7 years hand-on experience required

(2) Senior Base band Engineer
Job description:
Develop software interface and monitoring & control platform for ultra-wide bandwidth software defined radio according to the project requirement. Design, analyze, integrate, verify, and maintain digital hardware and circuit card assemblies.

Job requirement:
· Strong experience in software M&C design and power supply management for software defined radio (SDR) system. Design, analyze, integrate, verify, and maintain digital hardware and circuit card assemblies.
· Base band FPGA, CPLD and microprocessor design experiences are required.
Embedded programming skills & parallel processing with ultra high performance ARM processor and FPGA
· Responsibilities include board-level digital, power, analog design, analysis, and test. Design and simulate state of the art high density digital and analog PWB/PCBs using Pads from Mentorgraphics. Must be able to quickly assimilate and refine existing hardware designs;
· Good Knowledge in SDR baseband processing requirements of cellular handsets or base station, can work closely with RF and system engineers, and actively contribute toward the evolution of our products.
· Familiarity with modems and different I/O interfaces
· Outstanding communications skills, oral and written
· Strong knowledge in digital communication is preferred.

Education Requirements: BSEE above with 3~7 years hand-on experience required

Interested candidates, please email CV (in MS WORD) to liewtf@stee.stengg.com

Thanks & Best Regards
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---ST Electronics Group---